

FIGURE 1A
(PRIOR ART)

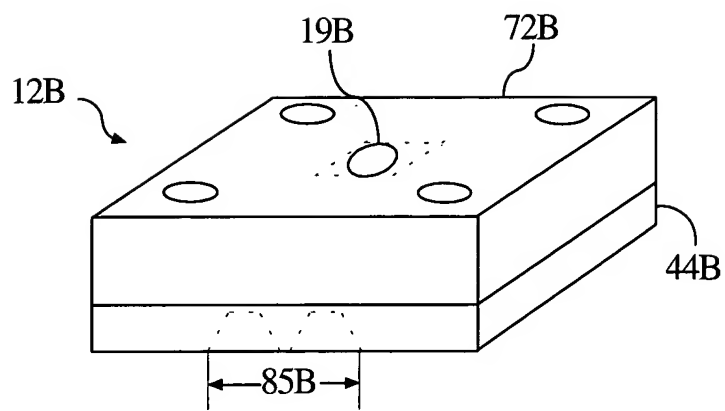


FIGURE 1B

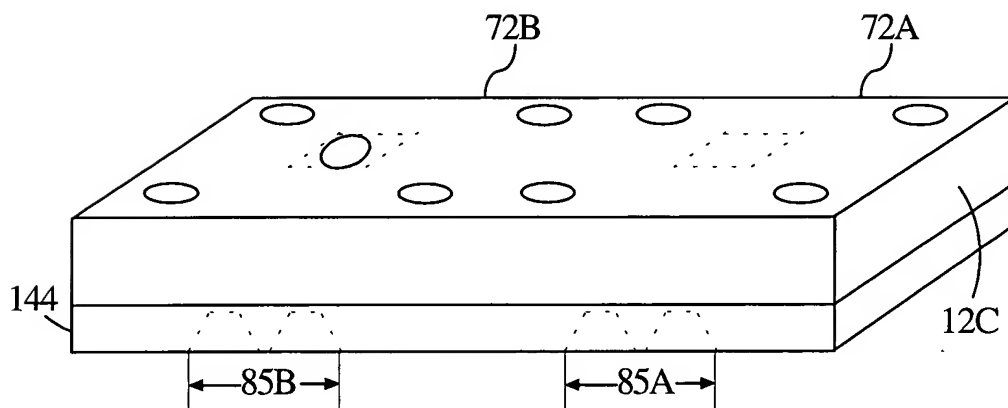


FIGURE 1C

FIG. 2 is a top view of a printed circuit board 300 according to one embodiment of the present invention.

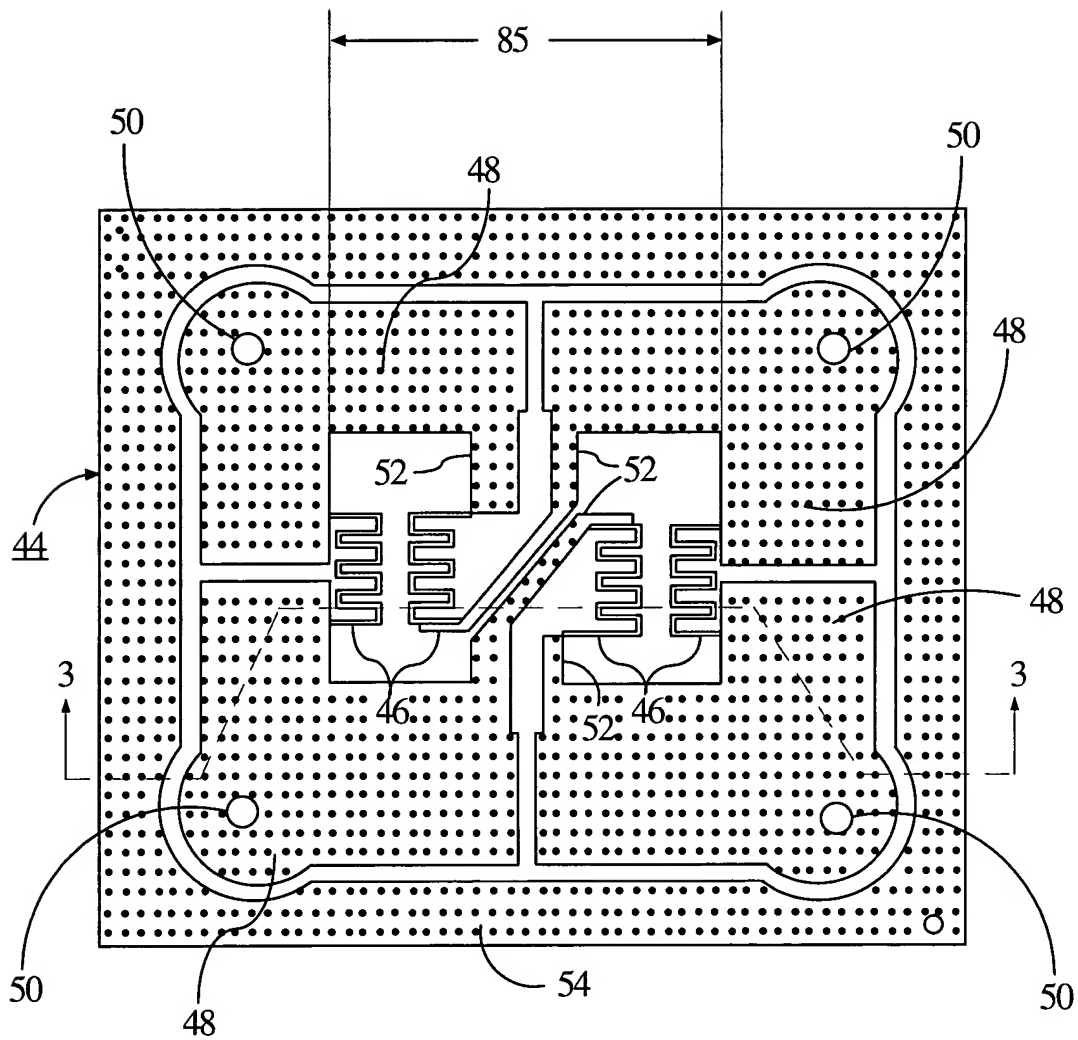


FIGURE 2

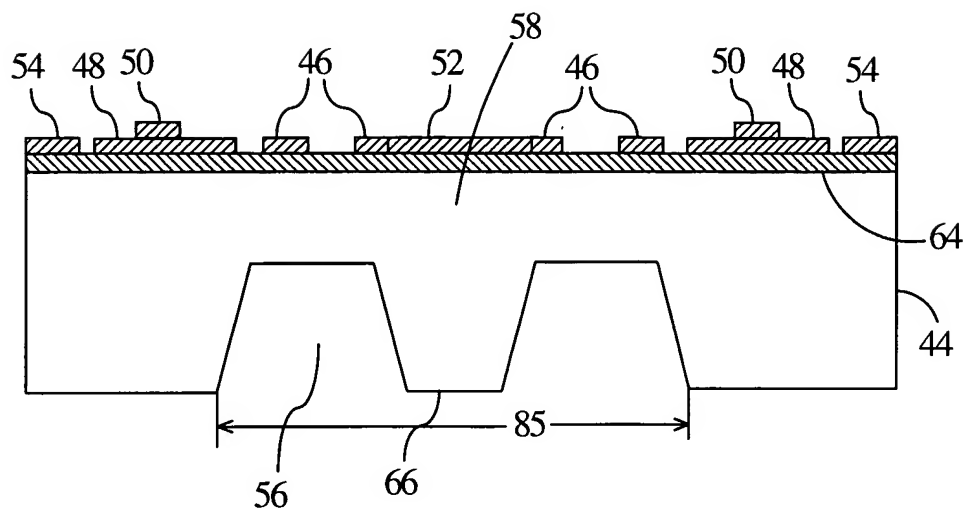


FIGURE 3

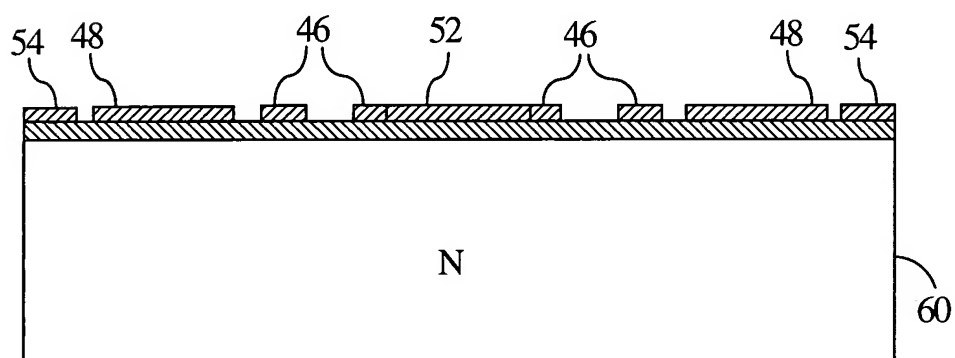


FIGURE 4A

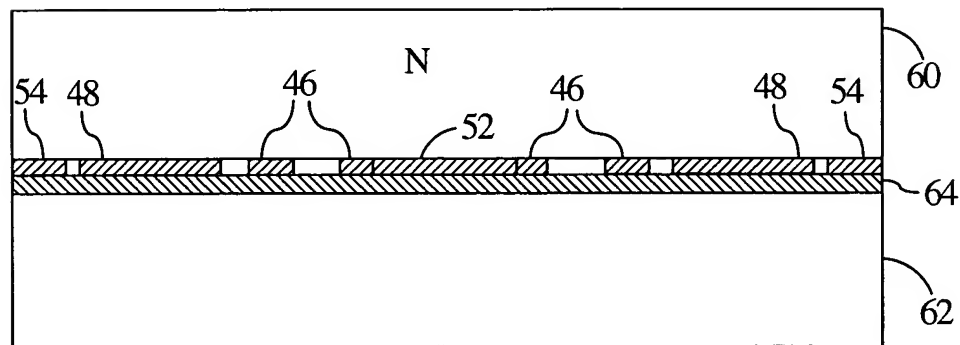


FIGURE 4B

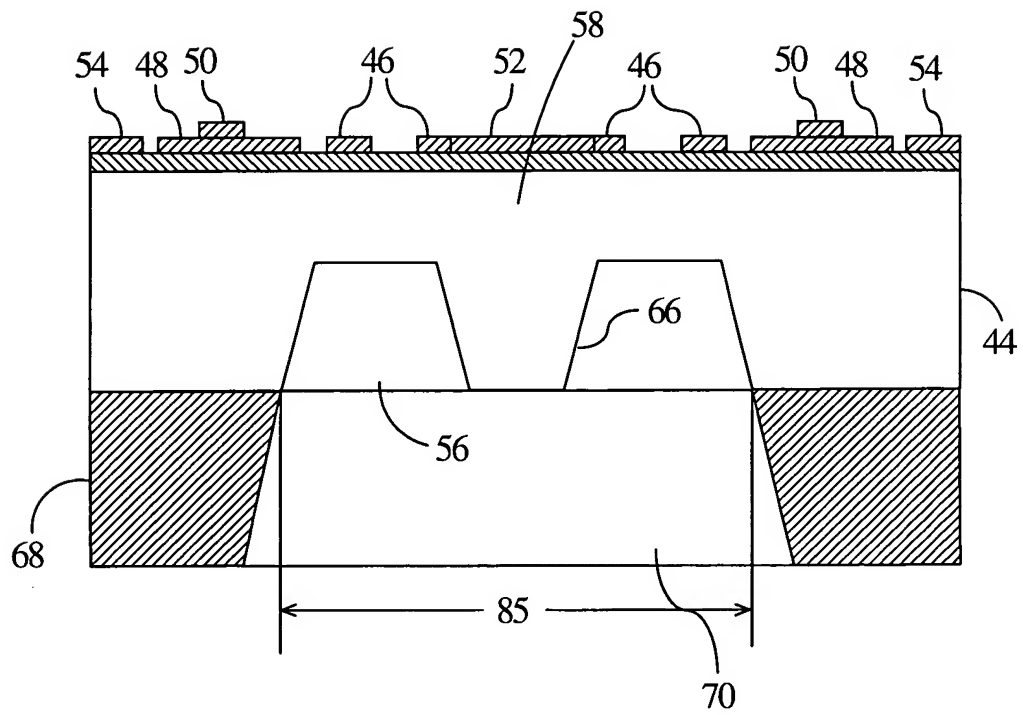


FIGURE 4C

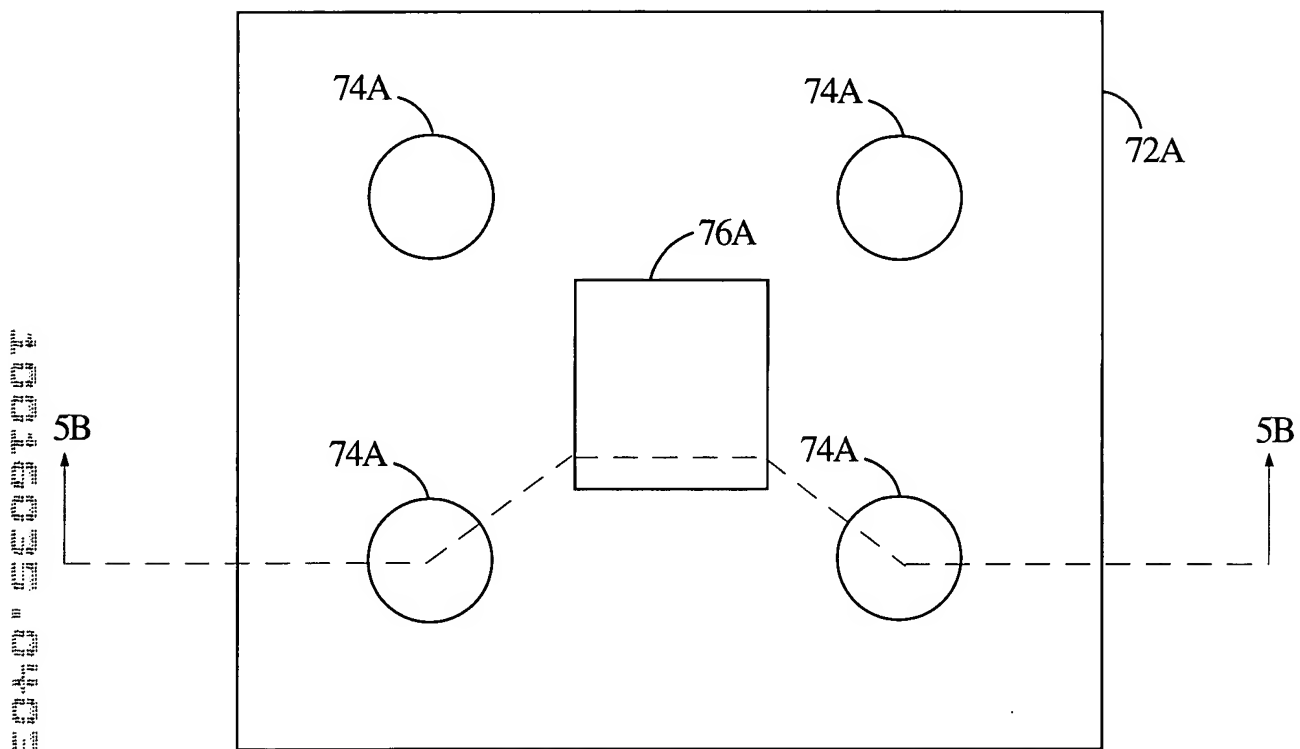


FIGURE 5A

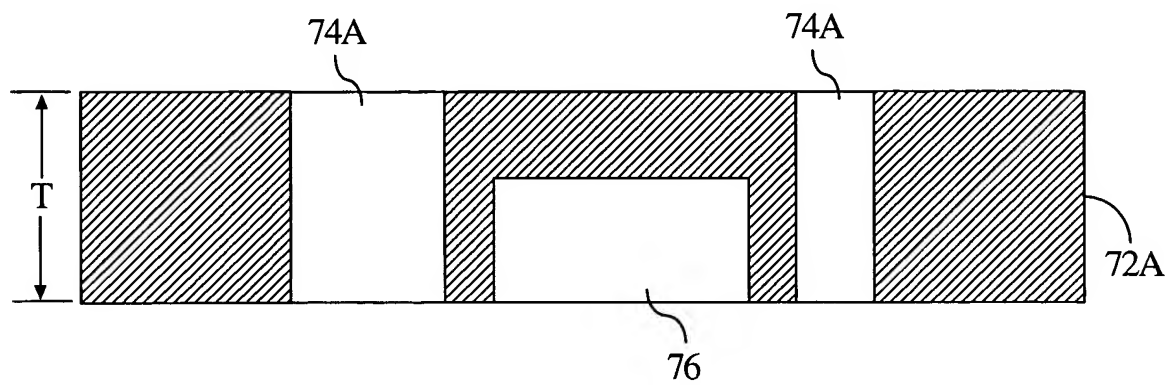


FIGURE 5B

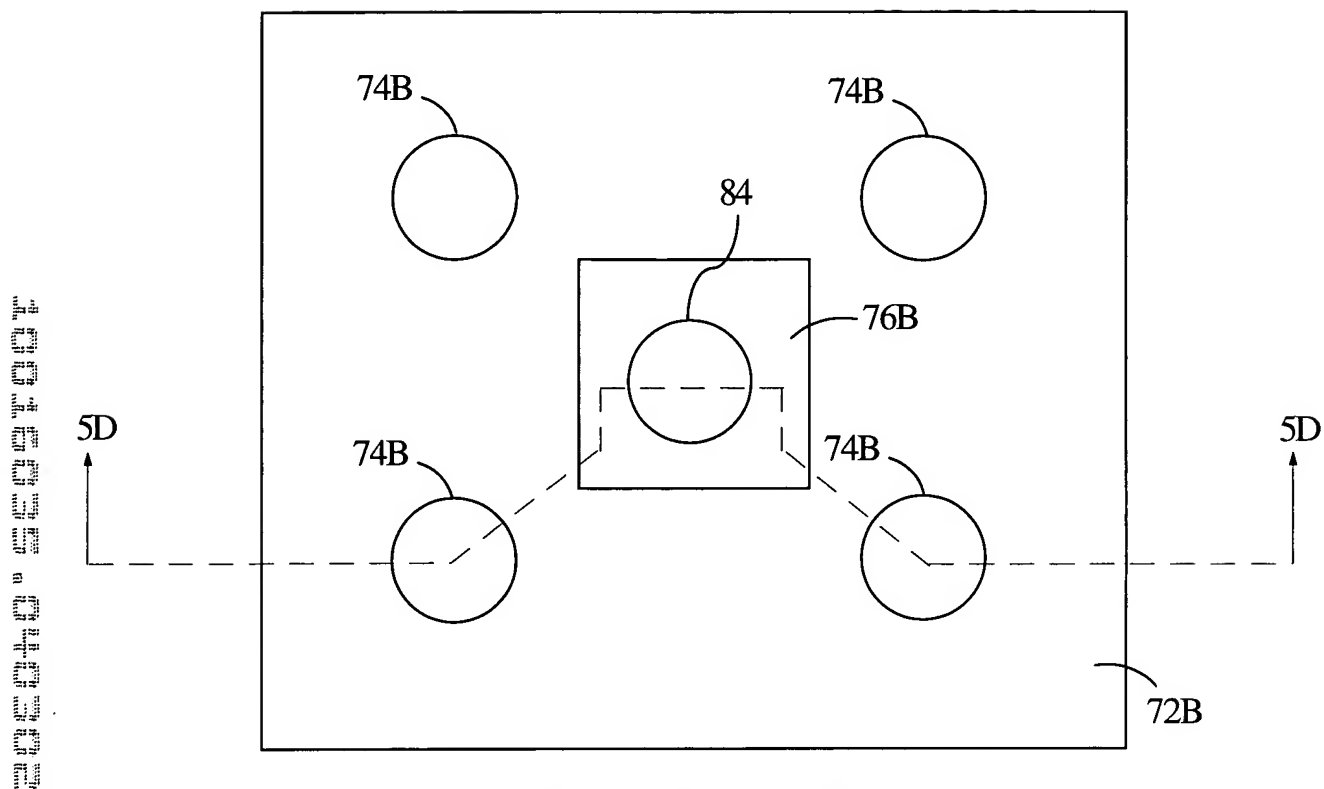


FIGURE 5C

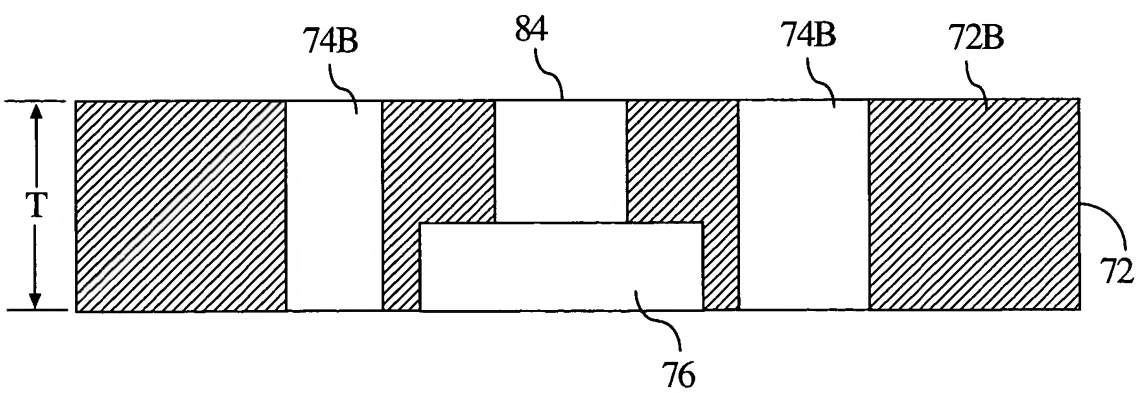


FIGURE 5D

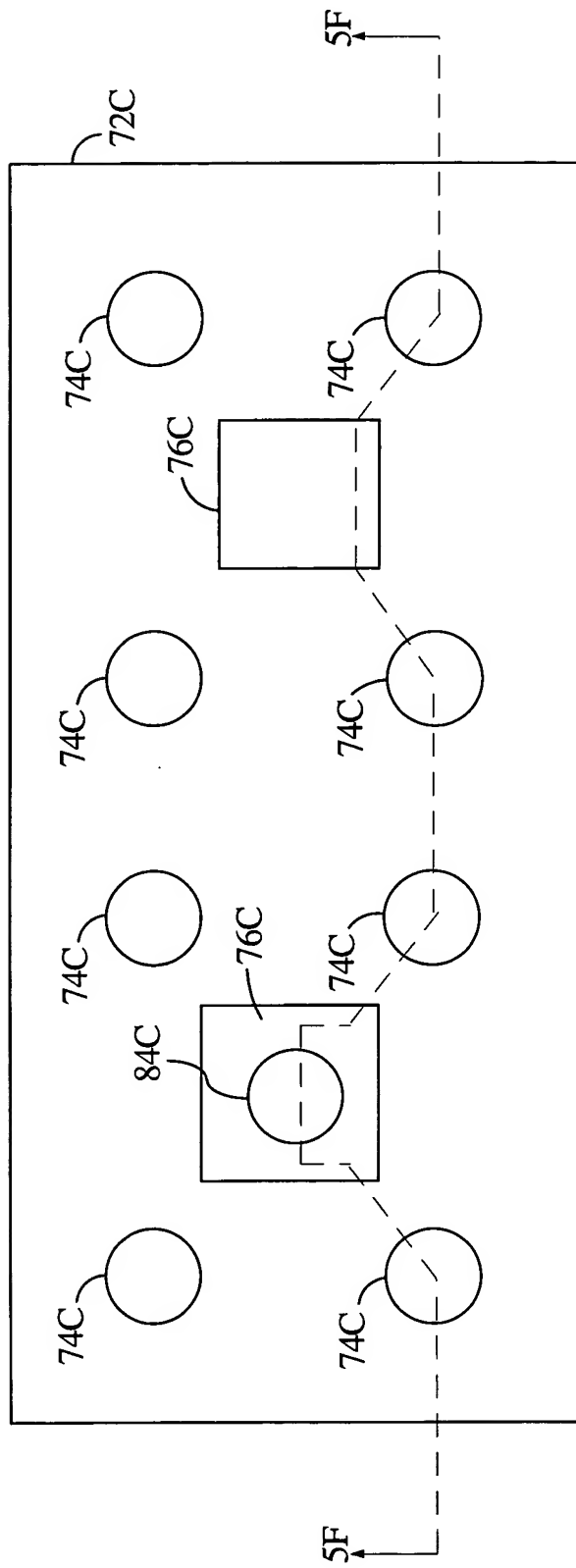


FIGURE 5E

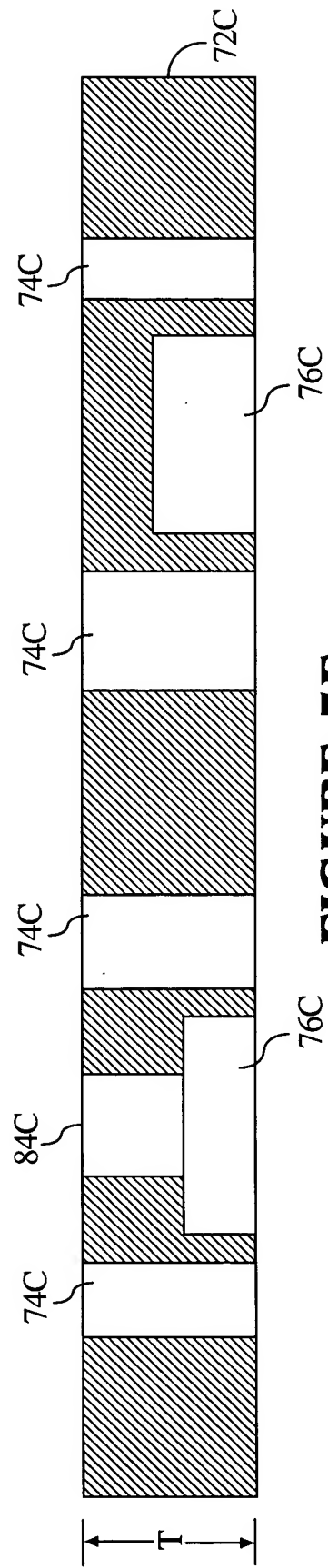


FIGURE 5F

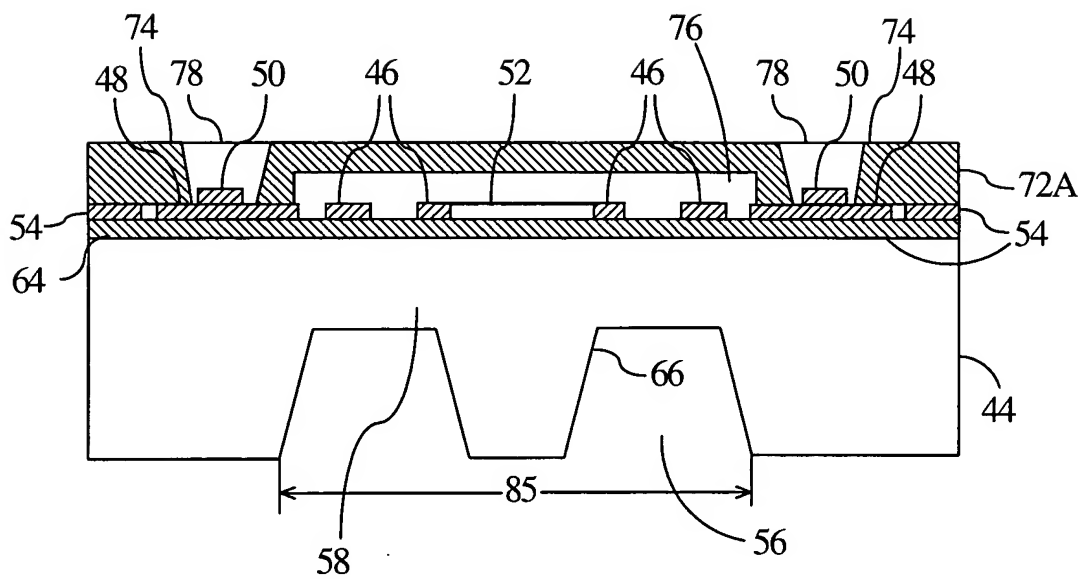


FIGURE 6A

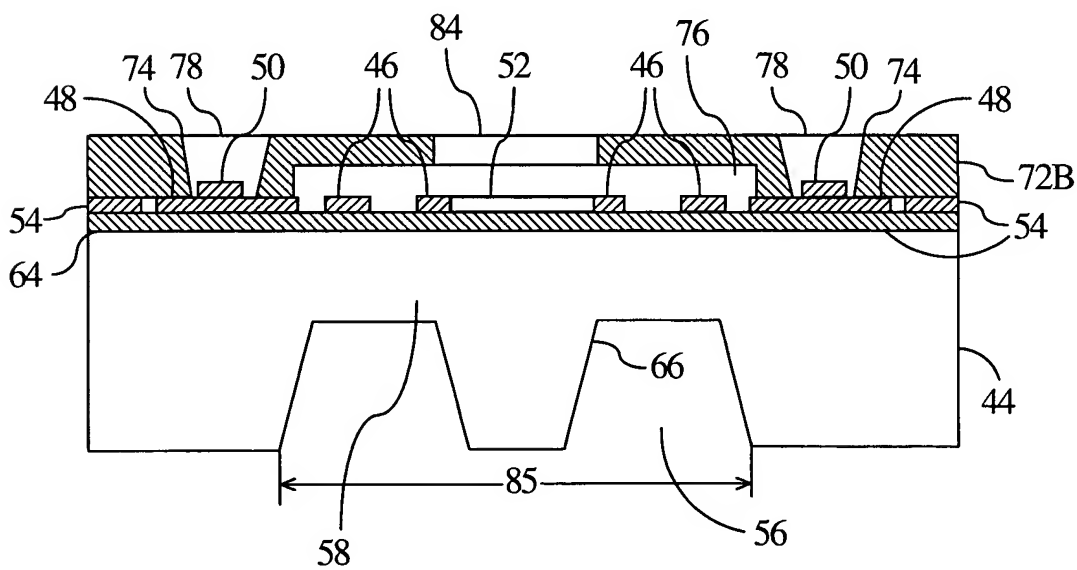


FIGURE 6B

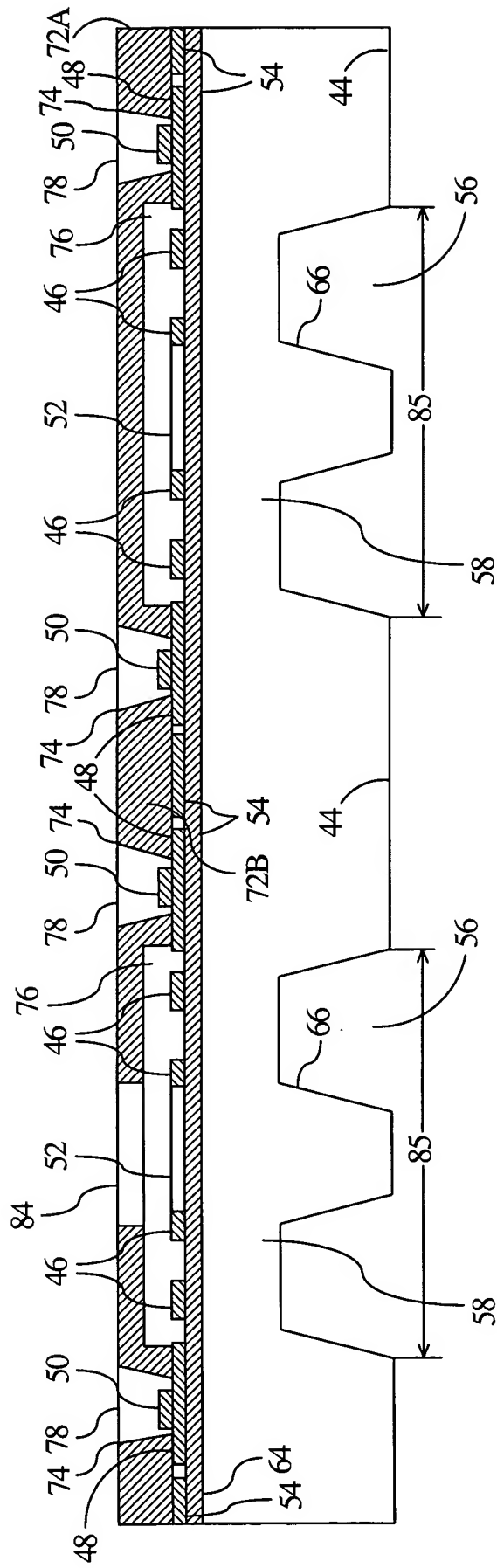


FIGURE 6C

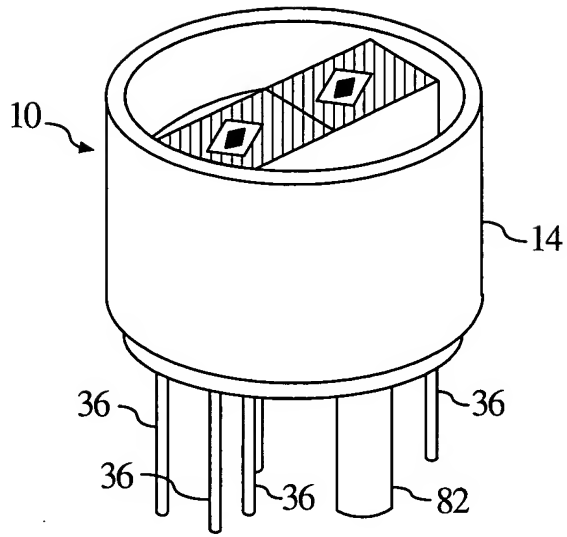


FIGURE 7A

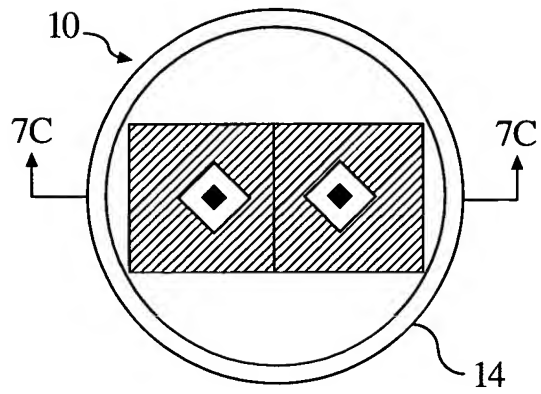


FIGURE 7B

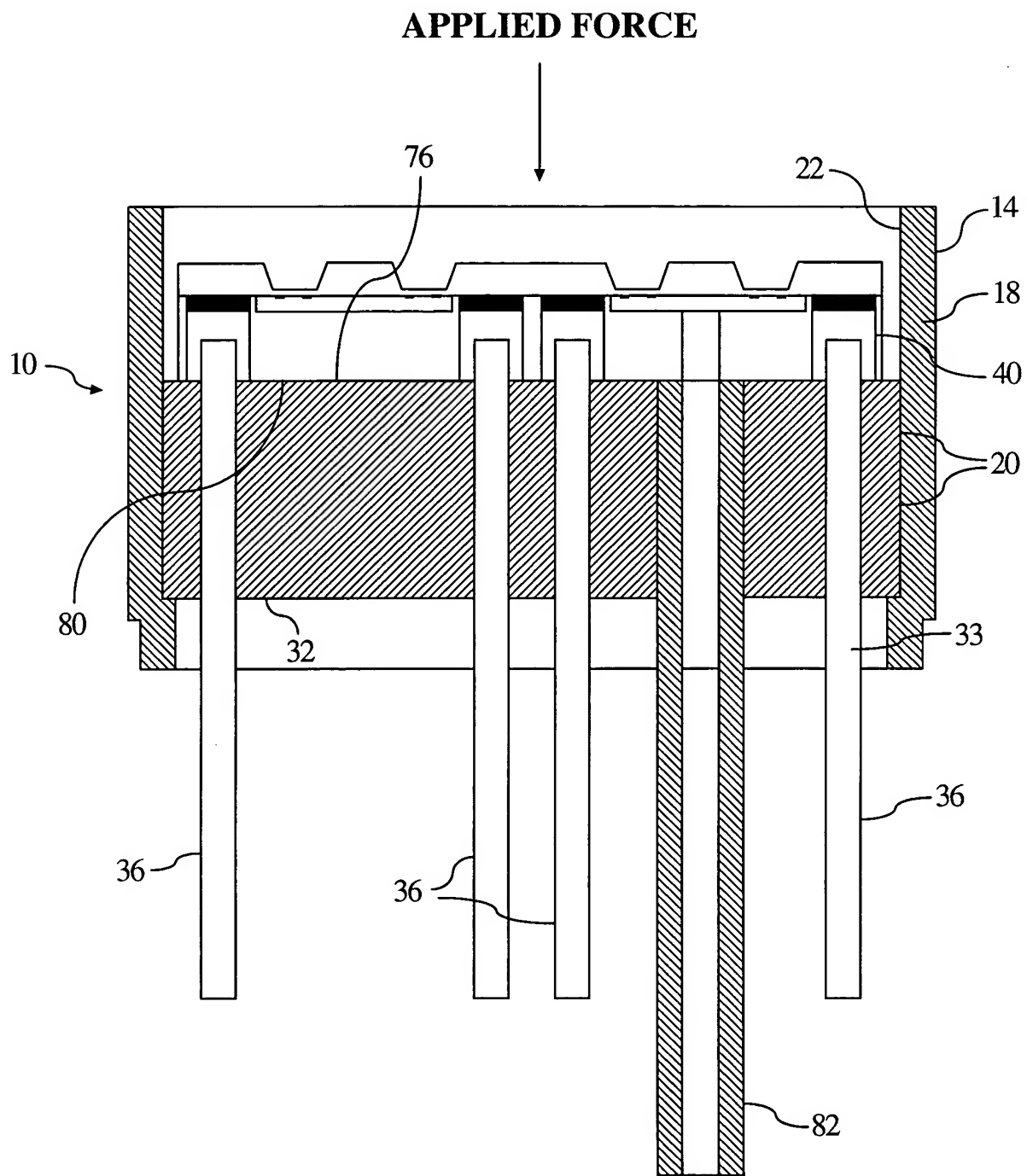


FIGURE 7C

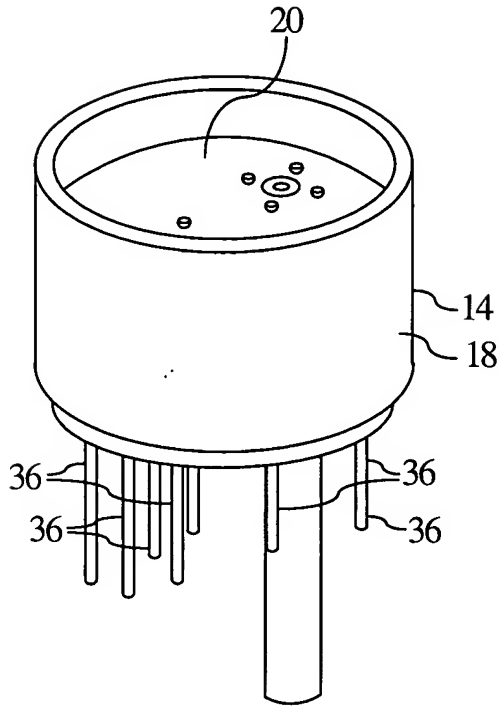


FIGURE 8A

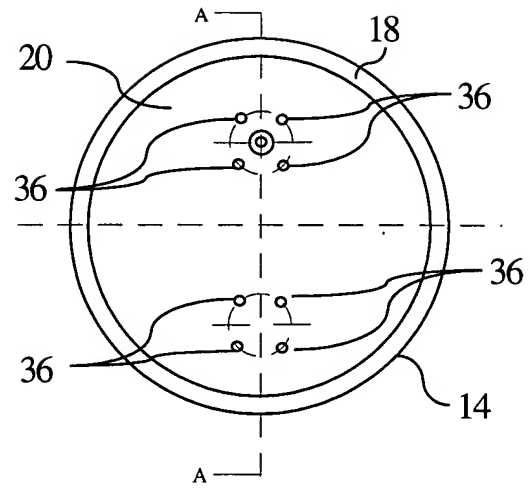


FIGURE 8B

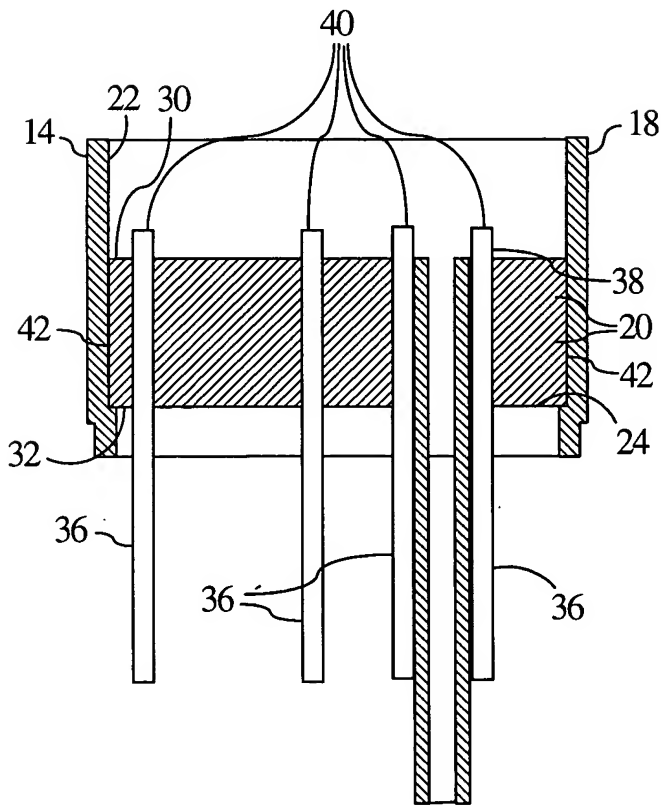


FIGURE 8C

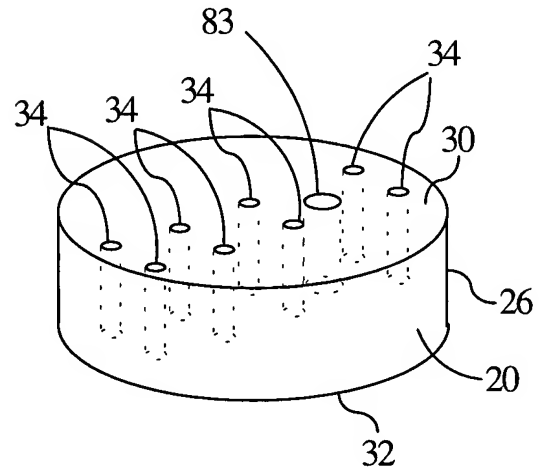


FIGURE 8D